

ITRI

Industrial Technology
Research Institute

PQC-Enabled Security Dongle: Hybrid FIDO2 & X.509 Certificate Solution

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About ITRI (Industrial Technology Research Institute)

“Innovate for industry. Create value for society.”

Who We Are

- Founded in **1973**, under **Taiwan’s Ministry of Economic Affairs**
- One of Asia’s leading **applied technology R&D institutes**
- Incubated top global companies like **TSMC**

Core Expertise

- **ICT | Semiconductors | Smart Manufacturing**
- **Green Tech | Biomedical | AI & Smart Living**
- Driving innovation across **six major tech sectors**

Our Role

- Bridge **lab to market**
- Support **SMEs’ digital transformation**
- Nurture **startups and innovation**
- Partner in **national tech programs** (e.g. Net-Zero, Cybersecurity)

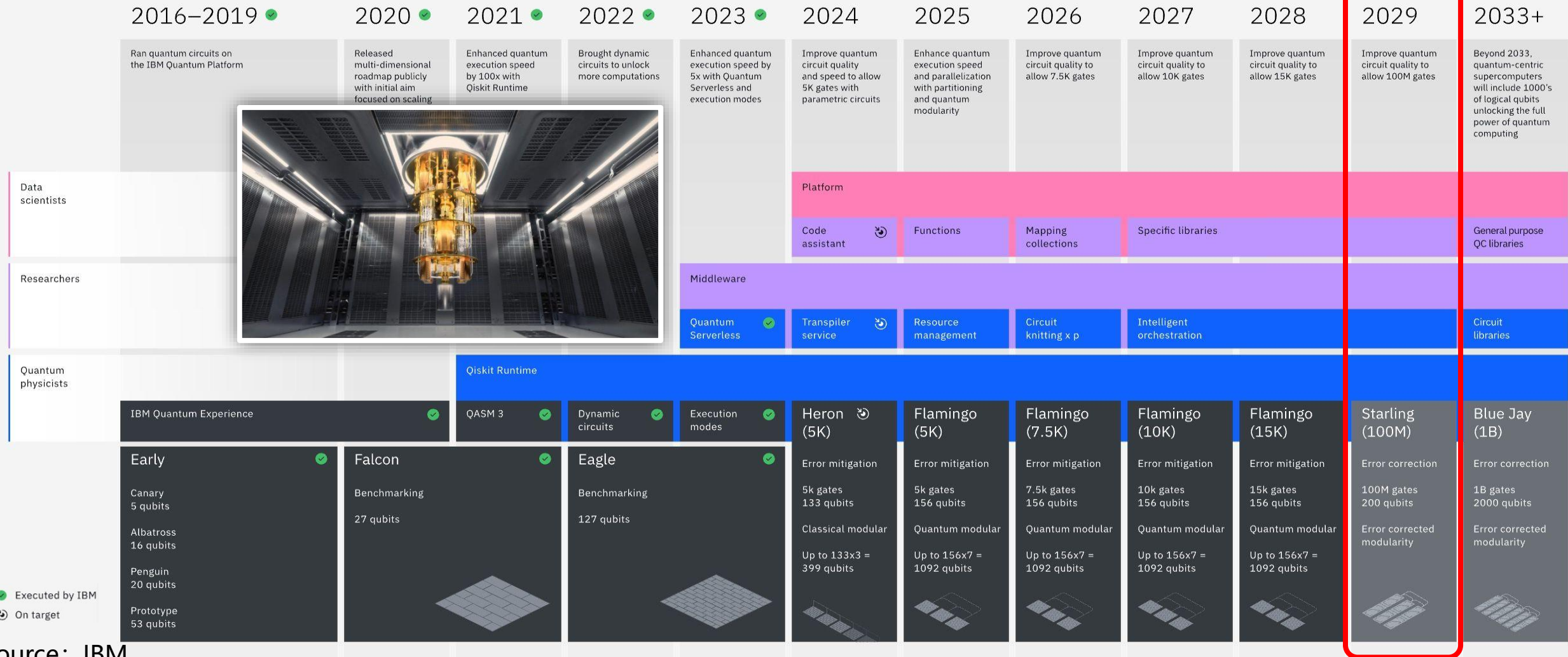


IBM Presents '2029 Million-Qubits' Roadmap

Development Roadmap

Q-day is getting closer!

IBM Quantum



✓ Executed by IBM
🎯 On target

Source: IBM

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Scaling Qubits with Modular Architecture

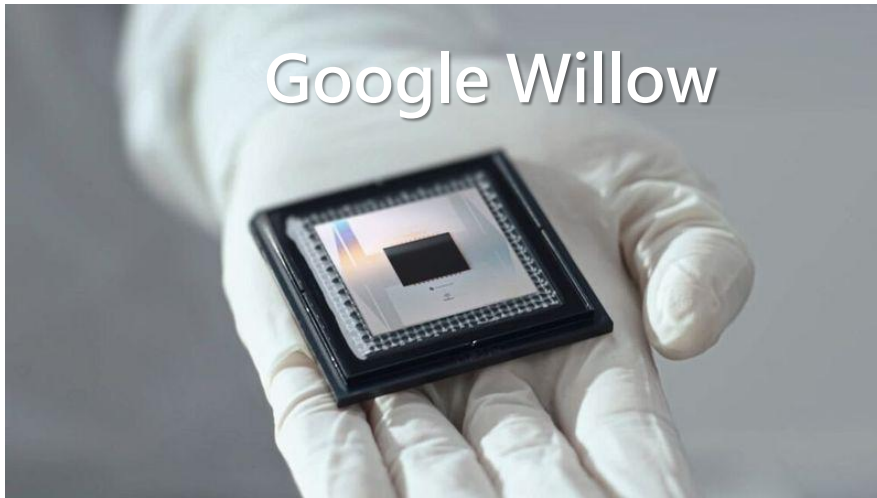
Microsoft Majorana 1



IBM Flamingo



Google Willow

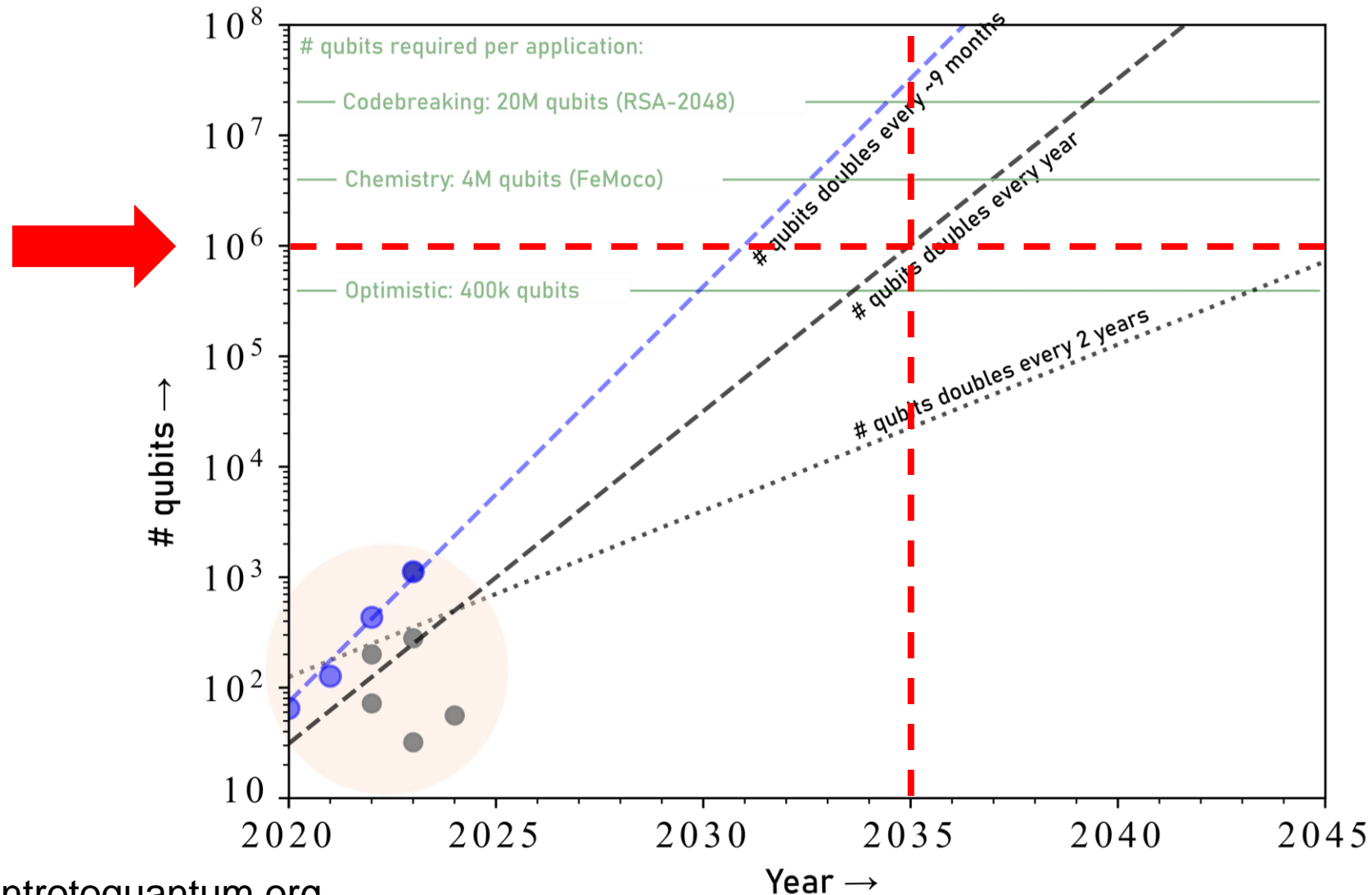


Amazon Ocelot



Moore's Law : 2035 to See Million-Qubit Breakthrough

Qubit growth estimates, according to Moore's Law



Source : introtoquantum.org

Classical Algorithms Challenged: Breaking Time Reimagined

| Algorithm | Type | Size of Quantum Computer | Time Required |
|--------------------|------------|---------------------------------|---------------|
| DL with NIST P-256 | Public key | 6.8×10^7 Qubits (68M) | 1 Day |
| RSA 3072 | Public key | 6.4×10^8 Qubits (640M) | 1 Day |
| AES-128 | Symmetric | 10^{30} Qubits (1G) | 1 Year |



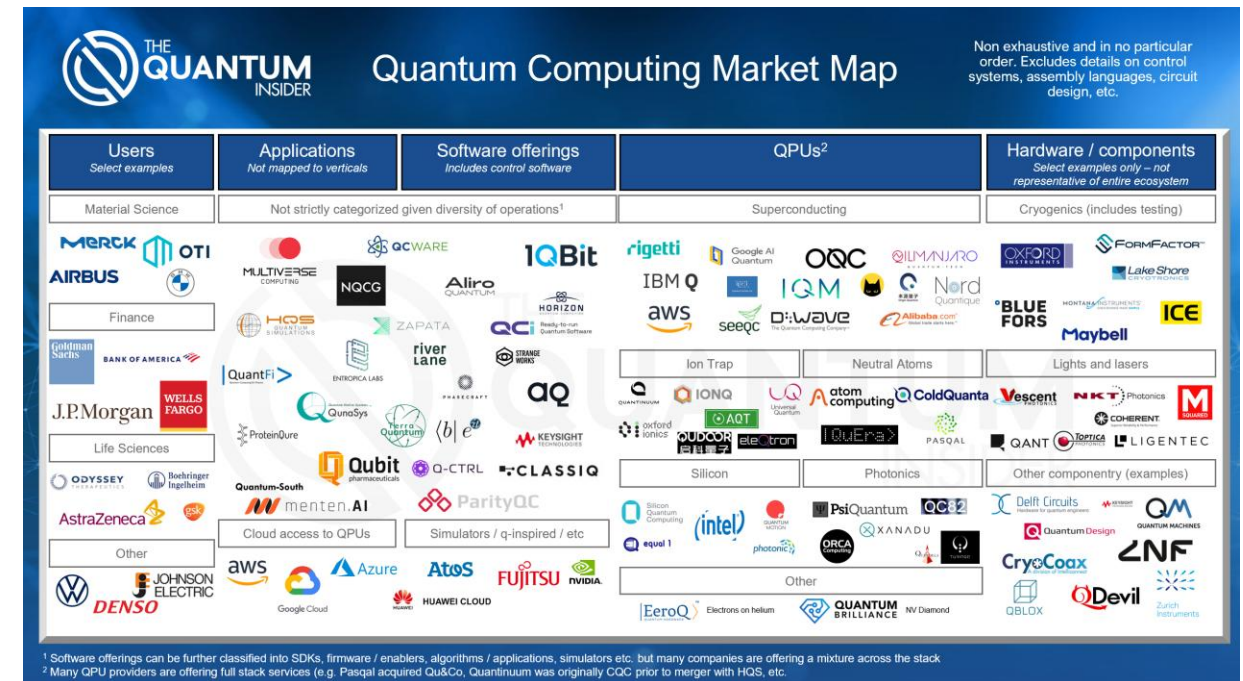
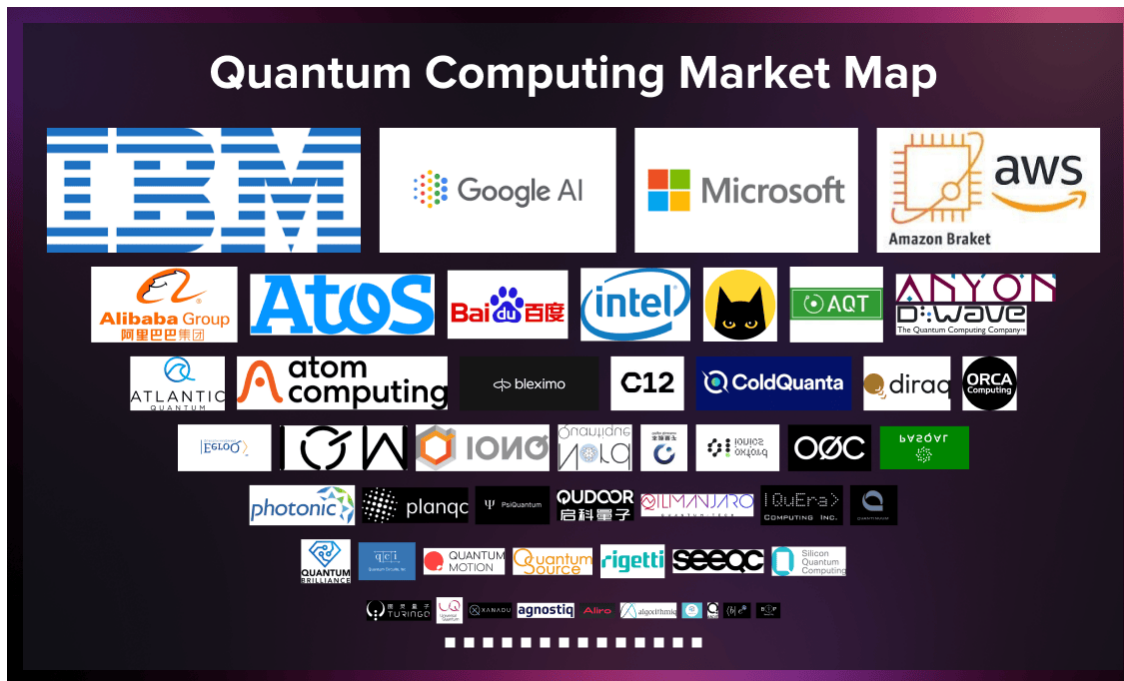
Shor's algorithm
Grover's algorithm



Source :
A Framework for Migrating to Post-Quantum Cryptography: Security Dependency Analysis and Case Studies (IEEE)

Harvest Now, Decrypt Later (HNDL)

Global companies are now involved in building quantum computing solutions—and, of course, even '**hacker applications**' are no exception!



source: serokell.io, thequantuminsider

Progress of NIST PQC Standardization

| PQC Algorithm | ★ ML-KEM (CRYSTALS-Kyber) | ★ ML-DSA (CRYSTALS-Dilithium) | ★ SLH-DSA (SPHINCS+) | FN-DSA (Falcon) | NEW HQC (Hamming Quasi-Cyclic) |
|--------------------------|---|--|---|---|---|
| FIPS Standard | FIPS 203 | FIPS 204 | FIPS 205 | FIPS 206 (Delayed) | Planned |
| Type | Key Encapsulation | Digital Signature | Digital Signature | Digital Signature | Key Encapsulation |
| Cryptography | Lattice-based | Lattice-based | Hash-based | Lattice-based | Code-based |
| Key Characteristics | <ul style="list-style-type: none"> • NIST's primary KEM standard • Good cross-platform performance • Relatively compact key sizes; fast operations • Efficient in both encryption and decryption • Suitable for embedded and IoT devices | <ul style="list-style-type: none"> • NIST's primary digital signature standard • Moderate key/signature sizes; fast signing and verification • Good security-performance balance • Suitable for high-performance and low-resource applications | <ul style="list-style-type: none"> • Alternate signature standard • Based on hash functions, highly reliable security • Large signature size, spatially limited • Stateless and security-stable | <ul style="list-style-type: none"> • Small signatures and public keys • Low bandwidth, fast verification • Complex key/signature generation (may involve floating-point ops) • Complex to implement; needs further validation for stability and side-channel resilience | <ul style="list-style-type: none"> • Candidate for code-based encryption • High computational overhead; best suited for high-resource environments • Large key sizes; unsuitable for low-power devices |
| Standardization Timeline | Finalized in August 2024 | | | Final draft of FIPS 206 expected post-2025 | Draft expected ~2026 Final ~2027 (tentative) |

Source: Topology Research Institute (TRI), compiled by ITRI, May 2025

Challenges of PQC Applications R&D

PQC
Algorithm is
Difficult



No Reference
Design



Interoperability
Testing

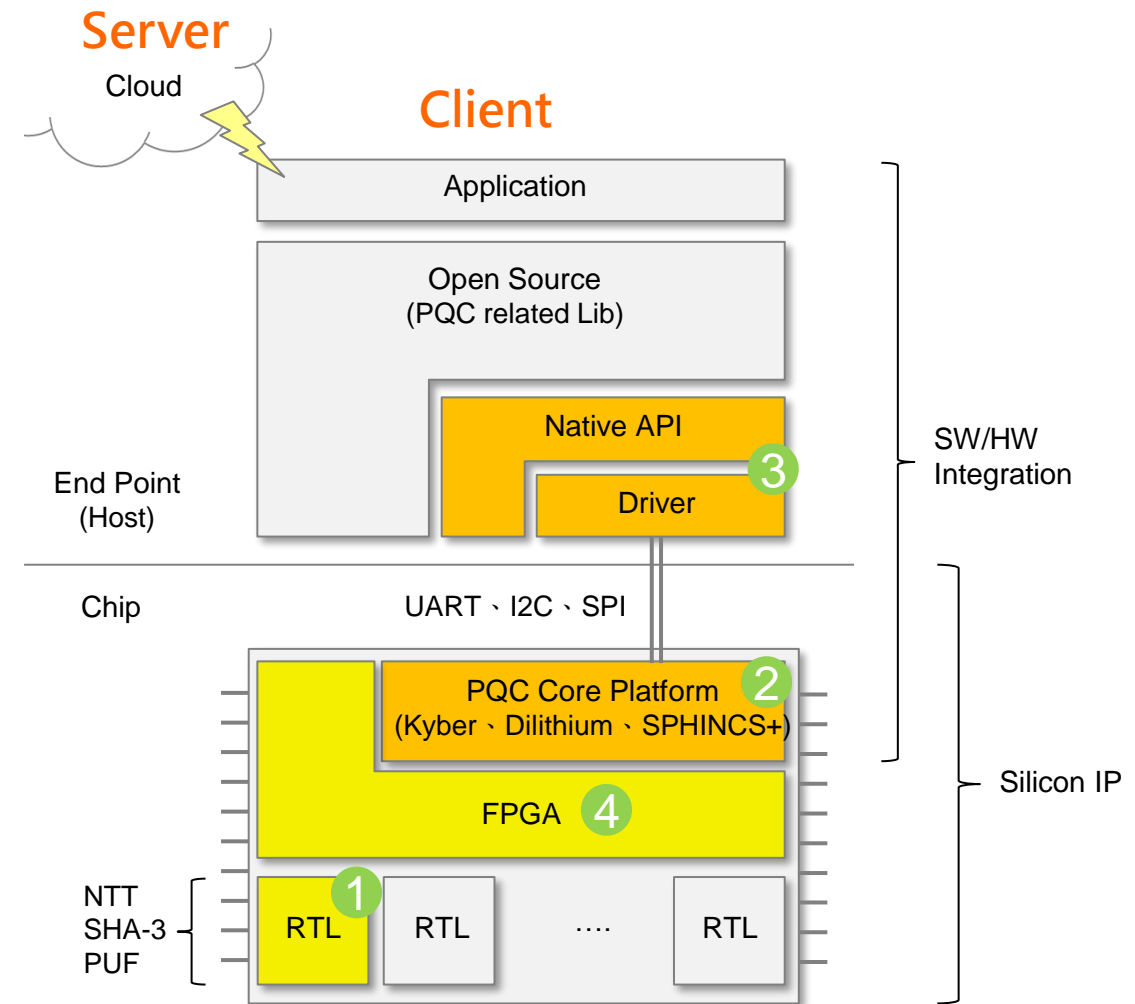


Budget



Lowering the Barriers: PQC Common Platform

- 1 Deployment of PQC on Both Server and Client →**
Provides a standard RTL circuit interface for integrating complete PQC algorithms or partial accelerator components.
- 2 Complexity of PQC Security Architecture →**
Offers PQC Core Algorithm Platform with NIST standardized algorithms to facilitate digital logic design verification.
- 3 Challenges of PQC Algorithm Libraries →**
Provides corresponding firmware algorithm libraries and APIs for easy industry application integration.
- 4 High Development Costs of ASIC Chips →**
Provides FPGA verification environment to assist in the feasibility validation of silicon IP and supports specialized chip product design.



PQC Common Platform and Application Use Cases

The PQC Common Platform Solution includes four key components: PQC Silicon Intellectual Property, PQC Software and Firmware, PQC Chip Design and Verification Environment, and PQC Application Reference Examples.

PQC Application Reference Examples.
(Identification 、 Digital Signature)

PQC Chip Common Platform for Product
Design and Verification Environment
(FPGA)

PQC IP
ML-KEM(Kyber) 、
ML-DSA(Dilithium) 、
SLH-DSA(SPHINCS+)

PQC SW/FW
ARM 、 RISC-V 、
X86 、 ...

All of the above are ready for collaboration



Identification



Digital Signature



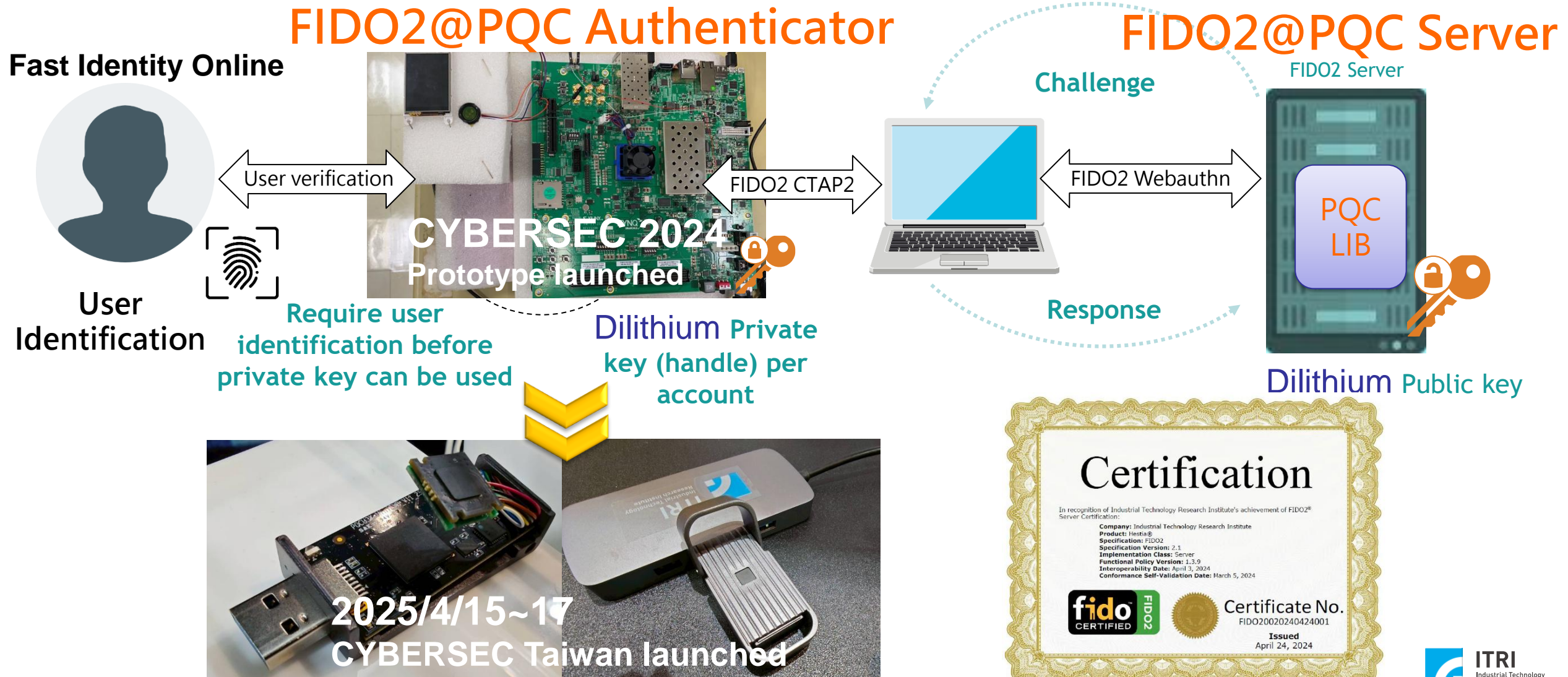
PQC IP & PQC SW/FW



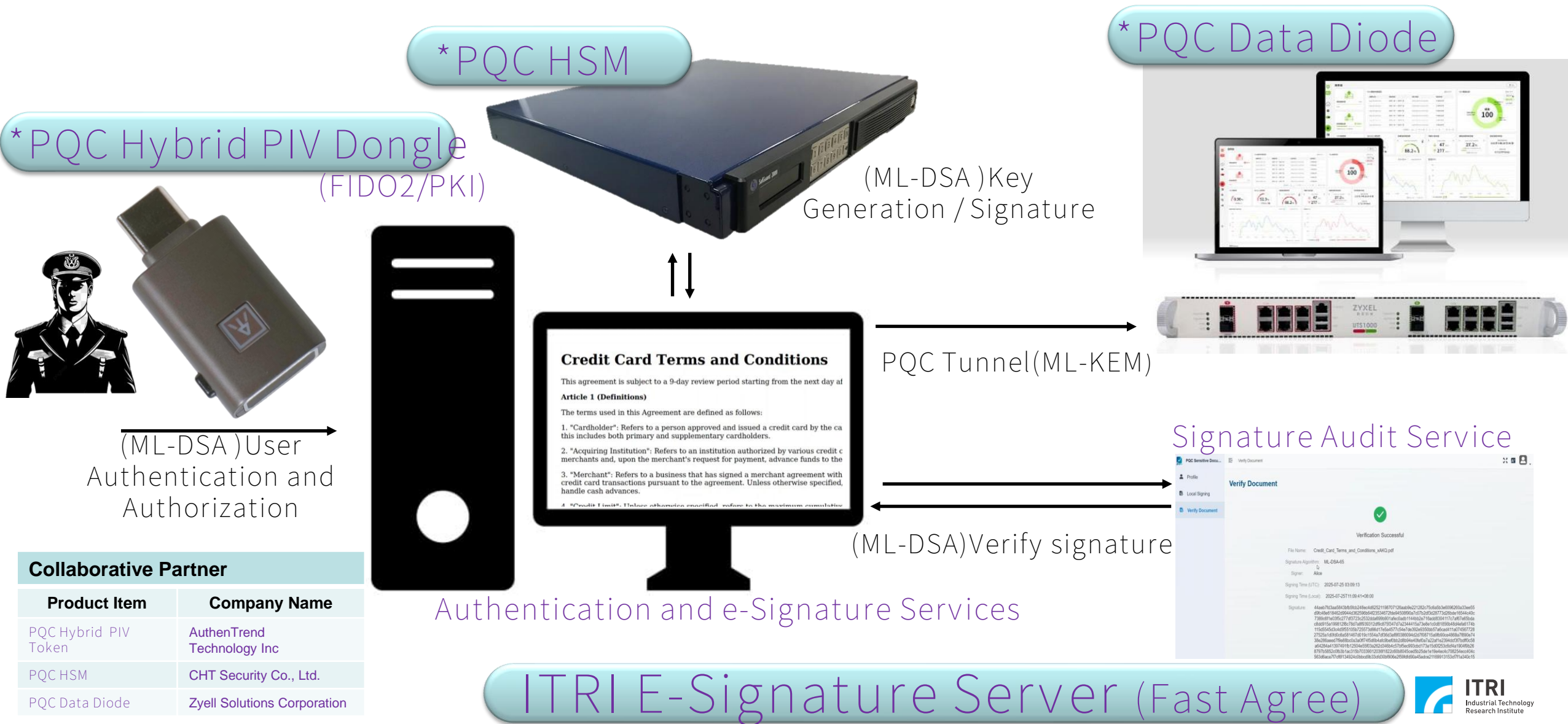
PQC Chip Common Platform

PQC Applications (Identification 、 Digital Signature)

ITRI Identity Authentication Solution

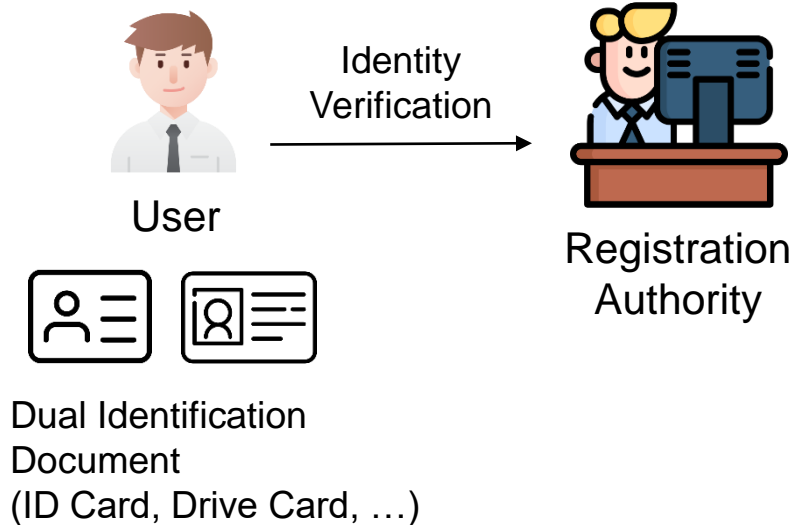


ITRI E-Signature Service Solution

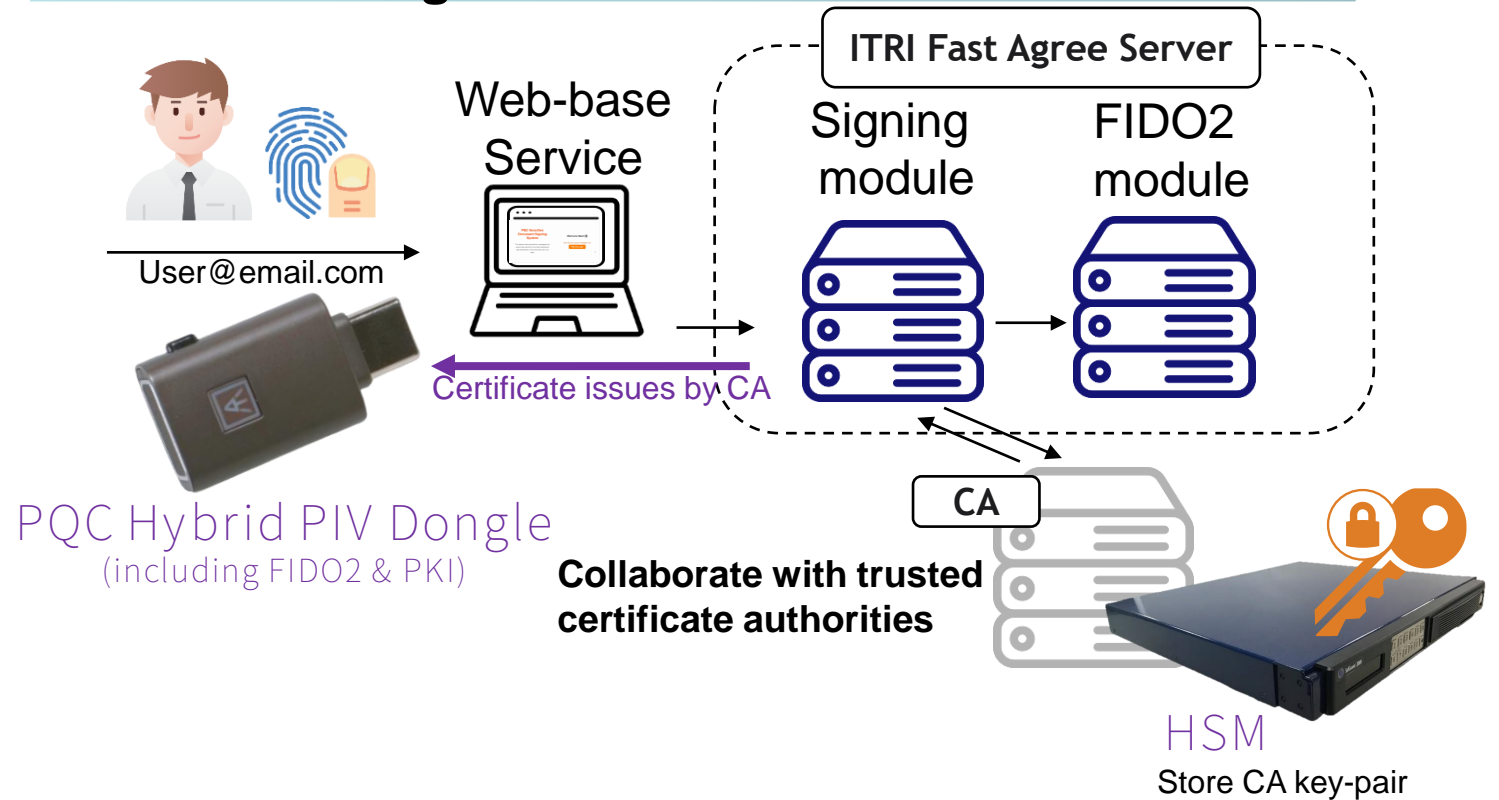


E-Signature System: Registration Process

Physical KYC for Customer Verification



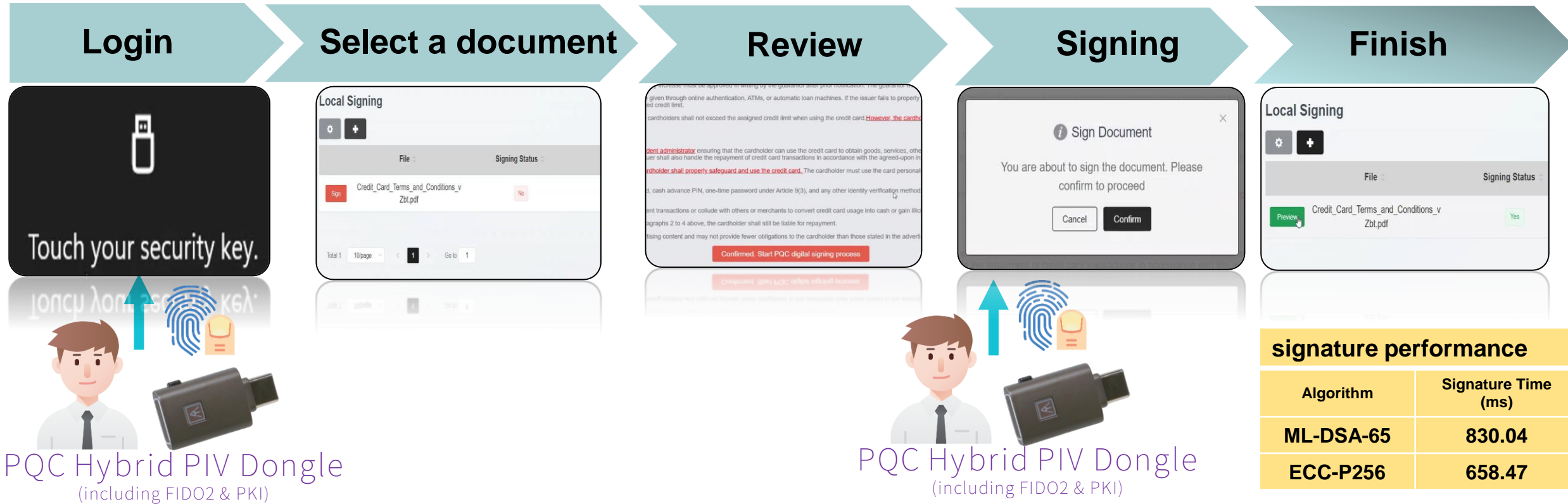
Secure Digital Identity Enrollment and Certificate Provisioning



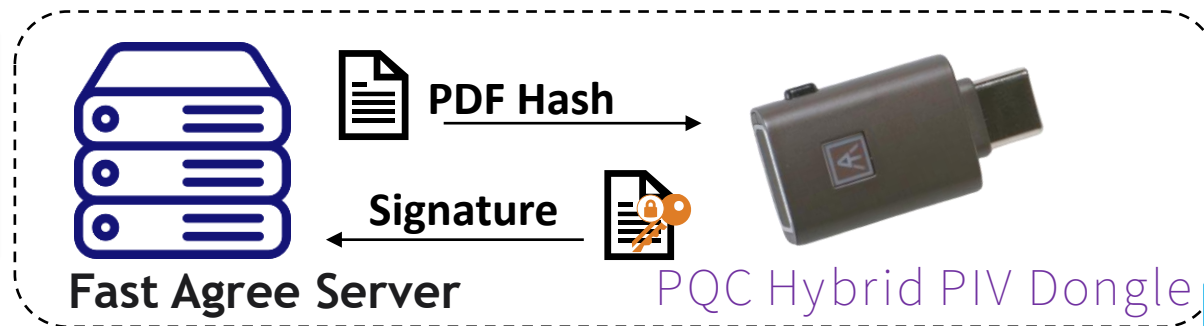
After completing dual ID verification, the user registers with "Fast Agree", obtains a digital certificate that issues by CA, and securely stores the user's private key and certificate in the PQC Hybrid PIV Dongle.

E-Signature System: Signing Process

support (ML-DSA-65 and ECC-P256 Signature)

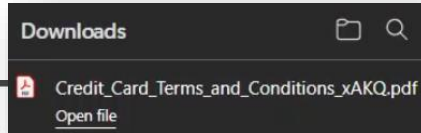


User logs in with fingerprint, picks a document, checks it, and signs with a PQC Hybrid PIV Dongle. It supports both ML-DSA65 and traditional ECC-P256 signature.



E-Signature System: Two Verification Process

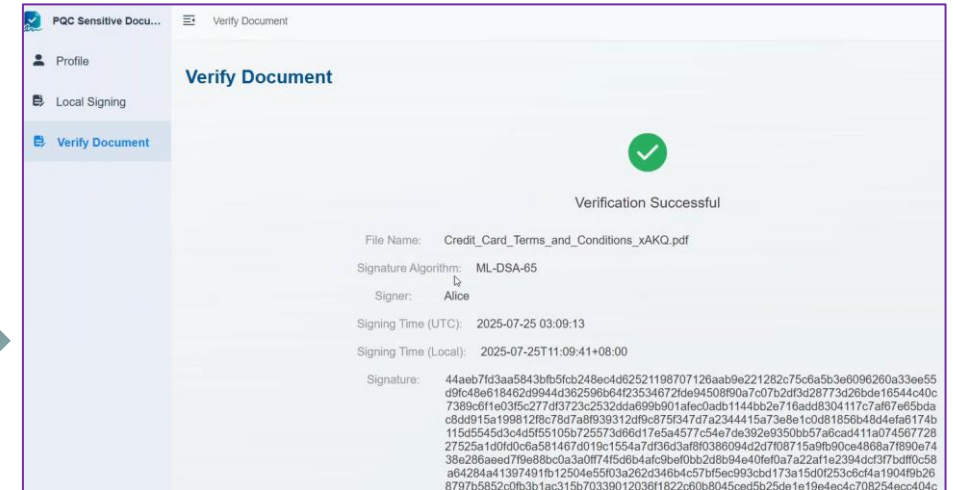
Download signed document



Upload signed document to verify with web-based verification tools

Note1: Signed using ML-DSA

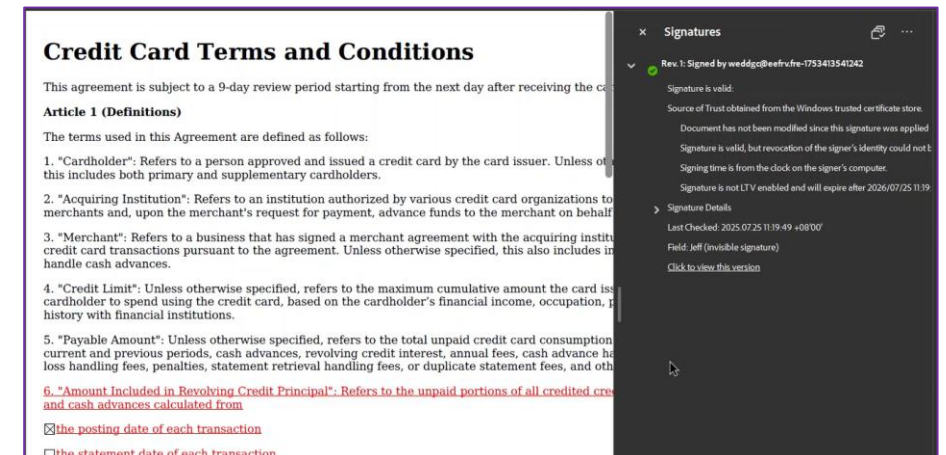
web-based verification tools



Open File with PDF Reader

Note2 Signed using traditional cryptographic algorithms (ECC-P256)

PDF reader verification



Users can verify signed documents using PDF Reader, or through the web-based verification tool provided within Fast Agree.

ITRI PQC E-Signature Solution Demo

Using the PQC-Enabled Security Dongle



<https://www.youtube.com/watch?v=TJ5tDWpnxGs>



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THANKS FOR YOUR LISTENING

Inquiries on PQC, e-signatures, or PIV dongles are welcome.